

Highlights

- 6 Tri-Speed Ports w/ Layer 2/3 Switch Management
- IRIG-A/B/G Time Code Generator
- 1 IRIG DC Input 2 IRIG DC/1PPS Outputs & 2 IRIG AM Outputs
- Up to (4) IEEE-1588v2 Clocks w/ Grand Master Capability
- 8 Programmable Discrete Inputs/Outputs



Overview

The integrated Ethernet Switch (iES-6), is a rugged 6 port layer 2/3 managed gigabit Ethernet switch with end node timing & discrete signal capabilities for demanding test instrumentation environments on airborne, shipboard or mobile ground vehicles.

Programmable discrete outputs can be used to control end node devices, on or off the network, as well as feedback into the iES-6 from discrete inputs.

With multiple time sources and outputs the iES-6 provides end node device time signals. iES-6 contains a hardware based IEEE-1588v2 time engine able to drive the internal IRIG-A/B/G Time Code Generator (TCG) for time outputs. The IRIG-A/B/G input provides IEEE-1588 Grand Master/Master clock capabilities.

The integrated board stack architecture and front panel allow for user specific functions and connector interfaces to be implemented without compromising system architecture or integrity.

PTP Clock's Configuration

Local Clock Current Time	PTP Time	Clock Adjustment method	Synchronize to System Clock	Ports Configuration
1970-01-01T00:00:38+00:00 267,425,440	Internal Timer	Synchronize to System Clock	Ports Configuration	

Clock Default DataSet

Clockid	Device Type	2 Step Flag	Ports	Clock Identity	Dom	Clock Quality	Pri1	Pri2	Protoco
0	Ord-Bound	True	8	30:2d:e8:ff:fe:11:12:34	0	Cl:251 Ac:Unknwn Va:65535	128	128	Ethernet

Clock Current DataSet

stpRm	Offset From Master	Mean Path Delay	DelayFilter	period	dist
0	0.000,000,000	0.000,000,000	6	1	2

Clock Parent DataSet

Parent Port Identity	Port	PStat	Var	ChangeRate	Grand Master Identity	Grand Master Clock Quality	Pri1	Pri2
30:2d:e8:ff:fe:11:12:34	0	False	0	0	30:2d:e8:ff:fe:11:12:34	Cl:251 Ac:Unknwn Va:65535	128	128

Clock Time Properties DataSet

UtcOffset	Valid	leap59	leap61	Time Trac	Freq Trac	ptp Time Scale	Time Source
0	False	False	False	False	False	True	160

Servo Parameters

Display	P-enable	I-enable	D-enable	'P' constant	'I' constant	'D' constant
False	True	True	True	3	80	

Unicast Slave Configuration

Index	Duration	ip address	grant	CommSta
0	100	0.0.0.0	0	IDLE
1	100	0.0.0.0	0	IDLE
2	100	0.0.0.0	0	IDLE
3	100	0.0.0.0	0	IDLE
4	100	0.0.0.0	0	IDLE

Specifications @ telspandata.com/iES6

- Non-Blocking Wire Speed Performance for All Frame Sizes Up to 9.6KB
- 4K VLAN's, 256 Filtering Policies
- 8K L2 Multicast Groups Addresses
- Port Mirroring, Link Aggregation CLI, Web GUI or SNMP Command, Control & Monitoring
- RS-232 COM Port

