iES-12
Ruggedized Ethernet Switch

Highlights

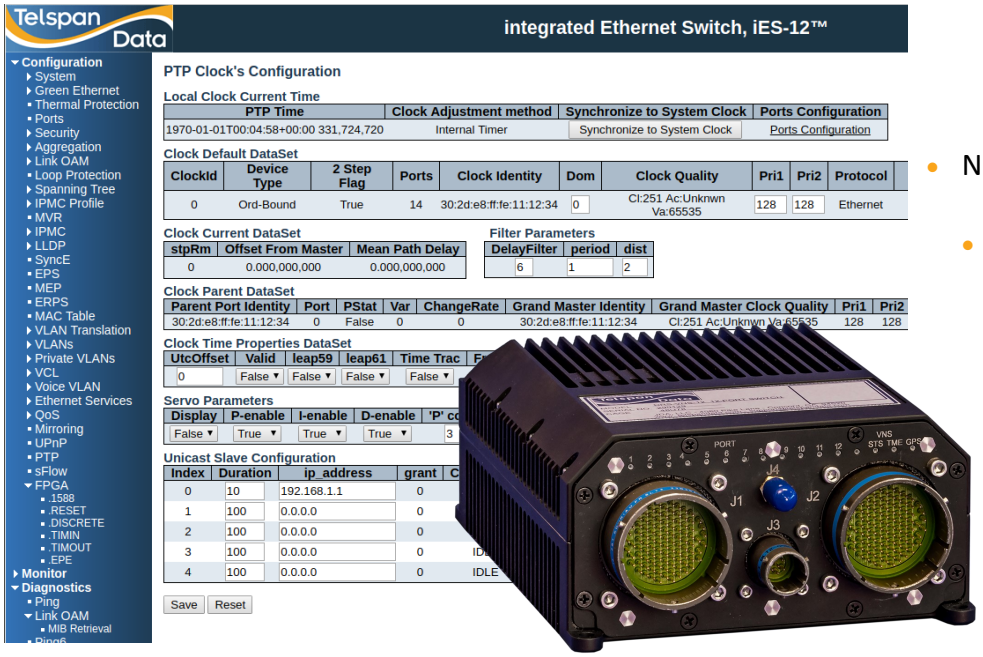
- 12 Tri-Speed Ports w/ Layer 2/3 Management
- Programmable FPGA w/ two 1Gbps Switch Interfaces
- HDLC/Chapter 7 PCM Encoder/Decoder
- GPS Receiver & IRIG-A/B/G Time Code Generator
- 3 IRIG DC/1PPS Outputs & 3 IRIG AM Outputs
- Up to (4) IEEE-1588v2 Clocks w/ Grand Master Capability
- Up to 16 Programmable Discrete Inputs/Outputs

Overview

The integrated Ethernet Switch (iES-12), is a rugged 12 port layer 2/3 managed gigabit Ethernet switch with end node timing, discrete signal capabilities for demanding environments on airborne, shipboard & ground vehicles.

A programmable FPGA tied directly into the switch with two 1G interfaces provides packet processing capabilities. Programmable or data driven discrete outputs can be used to control end node devices, on or off the network, as well as feedback into the iES-12 from discrete inputs. The FPGA also provides built-in HDLC & IRIG 106 Chapter 7 PCM encoder & decoder. This allows PCM output of VLAN, mirrored or filtered Ethernet switch traffic or from the PCM input decoded Ethernet back out the switch Ethernet ports.

With multiple time sources and outputs the iES-12 provides end node device IRIG time signals. iES-12 contains a high time accuracy internal GPS receiver & a hardware based IEEE-1588v2 time engine both able to drive the internal IRIG-A/B/G Time Code Generator (TCG) for time outputs.



Specifications @ telspandata.com/iES12

- Non-Blocking Wire Speed Performance for All Frame Sizes Up to 9.6KB
- FPGA for data processing, filtering & HDLC/CH7 PCM input/ouput
- 4K VLAN's, 256 Filtering Policies
- 8K L2 Multicast Groups Addresses
- Port Mirroring & Link Aggregation
- CLI, Web GUI or SNMP Control & Monitoring
- RS-232 COM Port