

Highlights

- 12 Tri-Speed Ports w/ Layer 2/3 Management
- Programmable FPGA w/ (2) 2.5Gbps Switch Interfaces
- HDLC/Chapter 7 Encoded PCM Output
- Internal GPS Receiver & IRIG-A/B/G Time Code Generator
- 4 IRIG DC/1PPS Outputs & 4 IRIG AM Outputs
- Up to (4) IEEE-1588v2 Clocks w/ Grand Master Capability
- Up to 16 Programmable Discrete Inputs/Outputs



Overview

The integrated Ethernet Switch (iES-12), is a rugged 12 port layer 2/3 managed gigabit Ethernet switch with end node timing, discrete signal capabilities & HDLC/IRIG 106 Chapter 7 encoded PCM output for demanding test instrumentation environments on airborne, shipboard or mobile ground vehicles.

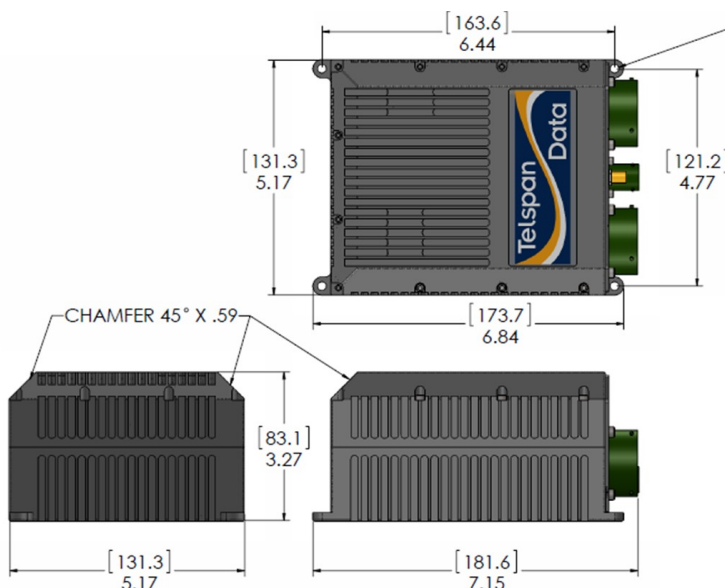
A programmable FPGA tied directly into the switch with 2.5Gbps interfaces provides packet processing capabilities. Programmable or data driven discrete outputs can be used to control end node devices, on or off the network, as well as feedback into the iES-12 from discrete inputs.

With multiple time sources and outputs the iES-12 provides end node device IRIG time signals. iES-12 contains a high time accuracy internal GPS receiver and a hardware based IEEE-1588v2 time engine both able to drive the internal IRIG-A/B/G Time Code Generator (TCG) for time outputs.

The integrated board stack architecture and front panel allow for user specific functions and connector interfaces to be implemented without compromising system architecture or integrity.

Specifications @ telspandata.com/iES12

- Non-Blocking Wire Speed Performance for All Frame Sizes Up to 9.6KB
 - 4K VLAN's, 256 Filtering Policies
 - 8K L2 Multicast Groups Addresses
 - Port Mirroring, Link Aggregation
- CLI, Web GUI or SNMP Command, Control & Monitoring
 - RS-232 COM Port



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